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**** CONTINUING DATA *******
 This application is a DIV of 10/149,667 10/09/2002 PAT 6,833,042
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**** FOREIGN APPLICATIONS *******
 JAPAN 2000-314861 10/16/2000

**** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ****
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Foreign Priority claimed <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	<input type="checkbox"/> Met after Allowance	STATE OR COUNTRY	SHEETS DRAWINGS	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119(a-d) conditions met <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No		JAPAN	7	12	12
Verified and /BRETT ALAN CROUSE/	Initials				
Acknowledged	Examiner's Signature				

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TITLE
 Method of manufacturing clad board for forming circuitry, clad board and core board for clad board

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